



THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of:

INVENTORS: Hall et al.

SERIAL NO.: 10/608,374

ART UNIT: 2661

FILED: 6/26/2003

EXAMINER: Unknown

TITLE: Efficient Circular Clipping Implementation Using Simplified CORDIC Rotators

ATTORNEY DOCKET NO.: 907A.0111.U1(US)

Commissioner For Trademarks

P.O. Box 1450

Alexandria, VA 22313-1450

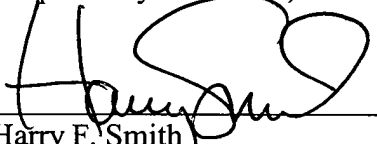
INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to Sections 609 and 707.05(b) of the MPEP and 37 CFR 1.97-1.99, copies are herewith submitted of documents which may be pertinent to the invention as claimed in the above-identified application. The attached form PTO-1449 lists these documents.

The citation of these documents should not be construed as a representation that a thorough search has been made, or that other, more pertinent material is not available.

Respectfully submitted,

  
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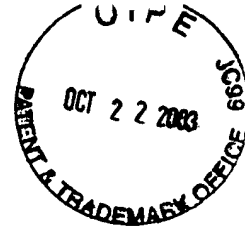
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**INFORMATION DISCLOSURE  
CITATION FORM FOR  
PATENT APPLICATION  
(FORM PTO-1449)  
(Substitute)**

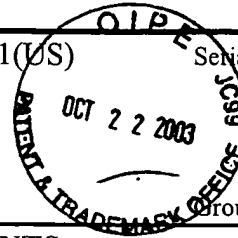
Docket No.: 907A.0111.U1(US)

Serial No.: 10/608,374

Applicant(s): Hall et al.

Filing Date: 6/26/2003

Group: 1056



**U.S. PATENT DOCUMENTS**

Examiner Initials	Document Number (Number-Kind Code)	Publication Date (MM-DD-YYYY)	Name of Patentee or Applicant	Class	Sub-class
	US-5,524,286	06-04-1996	Chiesa et al.	455	126
	US-6,266,320 B1	07-24-2001	Hedberg et al.	370	206
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**FOREIGN PATENT DOCUMENTS**

Examiner Initials	Document Number (Country Code-Number-Kind Code)	Publication Date (MM-DD-YYYY)	Name Of Patentee of Applicant	Translation? Yes/No/n/a
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**OTHER DOCUMENTS (Author (Capitalize), Title, Date, Pages, Etc., if known)**

	Andraka, Ray, "A survey of CORDIC algorithms for FPGA based computers",
	FPGA1998, Monterey, CA, 10 pages

Examiner's Signature:

Date Considered:

Initial if reference was considered, whether or not citation is in conformance with MPEP. Mark through citation if not considered.

Include a copy of this citation form with your next correspondence to the Applicant(s).